

Amendments to the Specification

Please replace paragraph [page 12, lines 12-19] with the following amended paragraph:

Here, in the related art, the cell switching unit 1 has information regarding VPI/VCI to be changed and routing information for all routing paths. Namely, for cells that are transferred to the cell processing unit 5, VPI/VCI change information of routing to Queue1 (10) and routing information for transfer from Queue2 (20) to the final destination are required. However, in the present invention, the cell switching unit 2 only needs to have a single VPI/VCI and routing information for any cell introduced from Queue3 (31). The cell switching unit 2 of the present invention can have appropriate hardware and/or software for adding the routing information for each ATM ~~ARM~~ cell

Please replace paragraph [page 13, lines 13-15] with the following amended paragraph:

Also, if the selected cell is a cell that is stored in Queue1 (11), the VPI/VCI of the corresponding cell is ~~changes~~ changed upon switching and the final destination is determined (S402,403).

Please replace paragraph [page 14, lines 10-16] with the following amended paragraph:

Namely, FIG. [[3]] 5 shows an ATM cell including a 5-byte (octet) header and a 48-byte payload. A .alpha.-byte (octet) routing information field is added in front of the header to thus form an ATM cell having a total of (53+.alpha.) bytes. As in the related art, the header comprises a 4-bit GFC (Generic Flow Control), an 8-bit VPI (Virtual Path Identifier), a 16-bit VCI (Virtual Channel Identifier), a 2-bit PT (Payload Type), a 2-bit CLP (Cell Loss Priority), and a 16-bit HEC (Header Error Control), also known as CRC (cyclic redundancy check).